

Time Table for Practical Exam, Dec - 2014

(Branch: *ELECTRONICS & COMM. ENGG.*)

Date of Exam	Subject Code	Name of Internal Examiner	Time for Written Test	Room No. / Lab for Witten Test	Room No/ Lab for Viva
12/12/2014	EC-303	Mr. AKASH AGRAWAL	10:00 AM to 11:00 AM	126, 128, 129	EDC Lab
15/12/2014	EC-305	Mr. PANKAJ AGRAWAL	10:00 AM to 11:00 AM	126, 128, 129	EDC Lab
16/12/2014	EC-306	Mr. DHARMENDRA GUPTA	10:00 AM to 11:00 AM	126, 128, 129	EDC Lab
18/12/2014	EC-304	MR. DHARMENDRA GUPTA	10:00 AM to 11:00 AM	126, 128, 129	EDC Lab

Year/ Semester: Third/ Fifth

Date of Exam	Subject Code	Name of Internal Examiner	Time for Written Test	Room No. / Lab for Witten Test	Room No/ Lab for Viva
15/12/2014	EC-505	Mr. PANKAJ AGRAWAL	10:00 AM to 11:00 AM	126, 128, 129	VLSI Lab
16/12/2014	EC-506	Mr. AKASH AGRAWAL	10:00 AM to 11:00 AM	126, 128, 129	VLSI Lab
17/12/2014	EC-503	Mr. ANURAG GARG	10:00 AM to 11:00 AM	126, 128, 129	VLSI Lab
18/12/2014	EC-504	Mr. KAPIL KESHWANI	10:00 AM to 11:00 AM	126, 128, 129	VLSI Lab

Year/ Semester: Final/ Seventh

Date of Exam	Subject Code	Name of Internal Examiner	Time for Written Test	Room No. / Lab for Witten Test	Room No/ Lab for Viva
12/12/2014	EC-703	Mr. ANURAG GARG	10:00 AM to 11:00 AM	126, 128, 129	EI Lab
15/12/2014	EC-705	Mr. KAPIL KESHWANI	10:00 AM to 11:00 AM	126, 128, 129	EI Lab
16/12/2014	EC-707	Mr. KAPIL KESHWANI	10:00 AM to 11:00 AM	126, 128, 129	EI Lab
17/12/2014	EC-706	Mr. PANKAJ AGRAWAL	10:00 AM to 11:00 AM	126, 128, 129	EI Lab
18/12/2014	EC-704	Mr. AKASH AGRAWAL	10:00 AM to 11:00 AM	126, 128, 129	EI Lab



MANOJ NARWARIYA

COE, IPS-CTM

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