

Time Table for Practical Exam, Dec - 2014

(Branch: ELECTRICAL ENGG.)

Year/ Semester: Second/ Third

Date of Exam	Subject Code	Name of Internal Examiner	Time for Written Test	Room No. / Lab for Witten	Room No/ Lab for Viva
15/12/2014	EE-306	Mr. SUMIT PATHAK	11:30 AM to 12:30 PM	107, 108, 108	Control System Lab
16/12/2014	EE-303	Mrs. SWATI RATHORE	11:30 AM to 12:30 PM	107, 108, 108	Control System Lab
17/12/2014	EE-305	Mr. MAHESHWARI PRASAD	11:30 AM to 12:30 PM	107, 108, 108	Control System Lab
18/12/2014	EE-304	Mr. ANUBHAV SAXENA	11:30 AM to 12:30 PM	107, 108, 108	Control System Lab

Year/ Semester: Third/ Fifth

Date of Exam	Subject Code	Name of Internal Examiner	Time for Written Test	Room No. / Lab for Witten	Room No/ Lab for Viva
15/12/2014	EE-501	Mr. NEERAJ PANDEY	11:30 AM to 12:30 PM	107, 108, 108	Power Electronics Lab
16/12/2014	EE-502	Mr. SANJAY KULSHRESHTHA	11:30 AM to 12:30 PM	107, 108, 108	Power Electronics Lab
17/12/2014	EE-506	Mr. ALOK PATHAK	11:30 AM to 12:30 PM	107, 108, 108	Power Electronics Lab
18/12/2014	EE-504	Mr. RAVI CHOURASIA	11:30 AM to 12:30 PM	107, 108, 108	Power Electronics Lab

Year/ Semester: Final/ Seventh

Date of Exam	Subject Code	Name of Internal Examiner	Time for Written Test	Room No. / Lab for Witten	Room No/ Lab for Viva
12/12/2014	EE-703	Mr. NEERAJ PANDEY	11:30 AM to 12:30 PM	107, 108, 108	Basic Electric Lab
16/12/2014	EE-704	Mr. ALOK PATHAK	11:30 AM to 12:30 PM	107, 108, 108	Basic Electric Lab
17/12/2014	EE-705	Ms. NEETU AGRAWAL	11:30 AM to 12:30 PM	107, 108, 108	Basic Electric Lab
18/12/2014	EE-701	Mr. SANJAY KULSHRESHTHA	11:30 AM to 12:30 PM	107, 108, 108	Basic Electric Lab



MANOJ NARWARIYA
COE, IPS-CTM
M - 08718805027